Amendment to the Specification

The Paragraph beginning at Page 30, lines 29-30, is to be amended as follows:

Figure 6 is a plan view showing a relationship between a set of the tags shown in Figure 6a 5a and a field of view of a netpage sensing device in the form of a netpage pen;

The Paragraph beginning at Page 40, lines 23-29, is to be amended as follows:

Various netpage coding schemes and patterns are described in the present applicants' co-pending US application USSN 09/575154 entitled "Identity-Coded Surface with Reference Points", filed 23 May 2000; co-pending US application USSN 10/120441 entitled "Cyclic Position Codes", filed 12 April 2002; co-pending US application USSN 10/309358 entitled "Rotationally Symmetric Tags", filed 4 December 2002; co-pending US Application USSN 10/409864 entitled "Orientation-Indicating Cyclic Position Codes", filed 9 April 2003; and co-pending US Application USSN 10/786,631—/———— entitled "Symmetric Tags", filed 4 March 2004 (Docket number NPT037).

The Paragraph beginning at Page 45, lines 6-8, is to be amended as follows:

Figure 54 shows the logical layout of another alternative hexagonal tag. This tag design is described in detail in the present applicants' co-pending US application USSN 10/786,631—/———— entitled "Symmetric Tags" (docket number NPT037US).

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The Paragraph beginning at Page 105, lines 28-31, through to Page 3, lines 1-8 is to be amended as follows:

The imaging unit incorporates both the image sensor 2412 and the image processor 2410, which are usefully combined into a single compact chip as described in the co-pending US applications USSN 10/778,056—/——— entitled "Image Sensor with Digital Frame Storestore", USSN 10/778,058 entitled "Image Sensor with Low-Pass Filter", USSN 10/778,060 entitled "Image Sensor with Range Expender", USSN 10/778,059 entitled "Pixel Sensor", USSN 10/778,063 entitled "Image Sensor for Timing Circuit", USSN 10/778,062 entitled "Image Processor with Low Power Mode", USSN 10/778,061 entitled "Image Processor", USSN 10/778,057 entitled "Synchronization Protocol" (docket no. NPS047 US—NPS054), all filed 17 February 2004.

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11.10.08